

## Lecture 8 - Sep 30

### Exceptions

*To Handle or Not to Handle: Versions 2, 3  
Generalizing Exception Handling*

## Announcements/Reminders

- Today's class: notes template posted
- Priorities:
  - + Complete **Lab1**; Due: This Tuesday (Sep 30)
  - + **Lab2** to be released
- ProgTest1
  - + guide released
  - + PracticeTest1 released
  - + In-Person Review Session at 2 PM, Friday, Oct 3 (CLH C)

# Version 1: B.mb Catches

```
class A {  
    A() {}  
  
    void ma(int i) throws NegValException {  
        if(i < 0) {  
            ⑦ println("abnormal exec of A.ma");  
            ⑧ throw new NegValException("Neg Val: " + i);  
        }  
        else {  
            ⑦ println("normal exec of A.ma");  
        }  
    }  
}
```

normal exec of A.ma

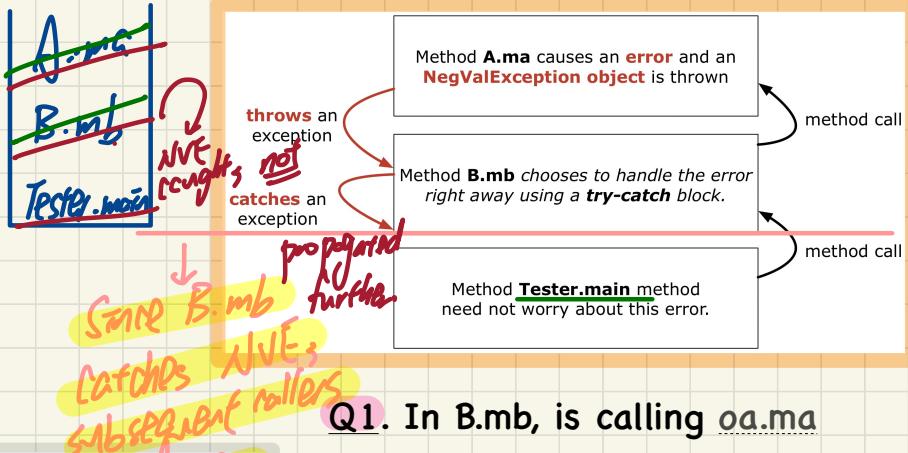
B.mb : calling A.ma  
did not cause NVE.

Tester.main: After  
calling B.mb.

abnormal exec. of A.ma

B.mb : calling A.ma  
caused NVE.

Tester.main: After calling B.mb.



```
class B {  
    B() {}  
  
    void mb(int i) {  
        ② A oa = new A();  
        try {  
            ③ oa.ma(i);  
            ④ println("From B.mb: Calling A.ma did not cause NVE.");  
        }  
        catch(NegValException nve) {  
            ⑤ println("From B.mb: Calling A.ma caused NVE.");  
        }  
    }  
}
```

```
class Tester {  
    main(...){  
        ① int i=5;  
        ② B ob = new B();  
        ③ ob.mb(i);  
        ④ println("From Tester.main: After calling B.mb.");  
    }  
}
```

Q1. In B.mb, is calling oa.ma  
subject to catch-or-specify req?

YES ; its callee A.ma  
specifies req.

Q2. In Tester.main, is calling B.mb  
subject to catch-or-specify req?

No ; its callee  
B.mb catches thcf.

# Version 2: B.mb Specifies

```
class A {
    A() {}

    void ma(int i) throws NegValException {
        if(i < 0) {
            println("abnormal exec of A.ma");
            throw new NegValException("Neg Val: " + i);
        }
        else {
            println("normal exec of A.ma");
        }
    }
}
```

```
class B {
    B() {}

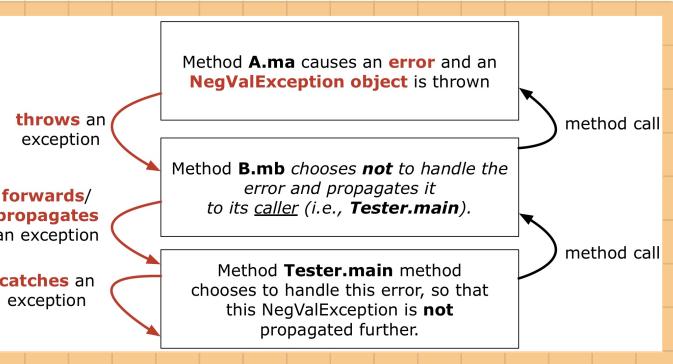
    void mb(int i) throws NegValException {
        A oa = new A();
        oa.ma(i);
        println("From B.mb: Calling A.ma did not cause NVE.");
    }
}
```

```
class Tester {
    main(...) {
        int i;
        B ob = new B();
        try {
            ob.mb(i);
            println("Tester.main: Calling B.mb did not cause NVE.");
        }
        catch(NegValException nve) {
            println("Tester.main: Calling B.mb caused NVE.");
        }
    }
}
```

4  
→  
5

Could be  
specified option  
(Version 3)

## Tester.main Catches



Q1. In B.mb, is calling oa.ma subject to catch-or-specify req?

Yes :: A.ma specifies it.

Q2. In Tester.main, is calling B.mb subject to catch-or-specify req?

Yes :: B.mb specifies it

# Version 3: B.mb Specifies

```

class A {
    A() {}

    void ma(int i) throws NegValException {
        if(i < 0) {
            println("abnormal exec of A.ma");
            throw new NegValException("Neg Val: " + i);
        }
        else {
            println("normal exec of A.ma");
        }
    }
}

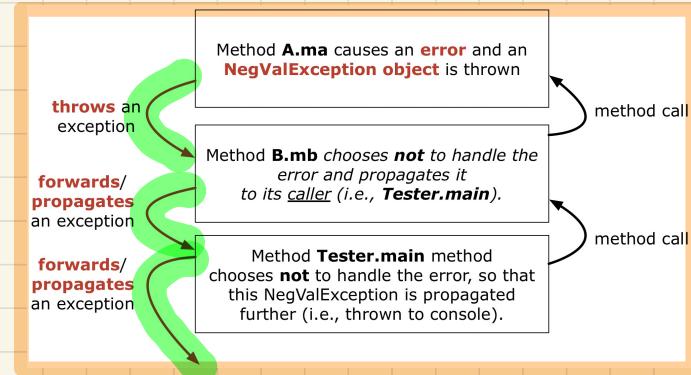
class B {
    B() {}

    void mb(int i) throws NegValException {
        A oa = new A();
        oa.ma(i);
        println("From B.mb: Calling A.ma did not cause NVE.");
    }
}

class Tester {
    main(...) throws NegValException {
        int i;
        B ob = new B();
        ob.mb(i);
        println("Tester.main: Calling B.mb did not cause NVE.");
    }
}

```

Tester.main Specifies



Q1. In B.mb, is calling oa.ma subject to **catch-or-specify req?**

Yes. A.ma specifies it

Q2. In Tester.main, is calling B.mb subject to **catch-or-specify req?**

Yes. B.mb specifies it.

# Catch-or-Specify Requirement: Call Stack

Q1. Origin of Exception:

$C_1.m1$

Q2. Methods subject to CoS Req:

$\lceil m \rceil$  methods ( $\lceil -1 \rceil$  methods specify, 1 method)

Q3. Methods free from CoS Req: (catch)

$n - \lceil m \rceil$  methods

Q4. Extreme Case 1 (exception handled earliest):

$C_2.m2$  catches the exception.

Q5. Extreme Case 2 (exception never handled):

$C_1.m1 \sim C_n.m_n$   $\not\equiv$  specify

